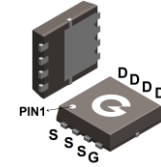
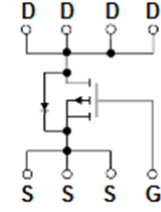


Features

- Advanced trench technology
- Low on-resistance
- Fast switching speed
- HBM: JESD22-A114-B: 1A
- RoHS compliant with Halogen-free

HF



PDFN3×3-8L

Mechanical Data

- Case: PDFN3×3-8L
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BL200P03-3DL8	PDFN3×3-8L	5000 pcs / Tape & Reel	200P03

Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	-30	V
Gate-to-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (T _C = 25°C)	I _D	-40	A
Continuous Drain Current (T _C = 100°C)		-25	
Continuous Drain Current (T _A = 25°C) *1		-6.7	
Continuous Drain Current (T _A = 100°C) *1		-4.2	
Pulsed Drain Current (t _p = 10μs, T _C = 25°C)	I _{DM}	-160	A
Single Pulse Avalanche Energy *3	E _{AS}	45	mJ
Power Dissipation (T _C = 25°C)	P _D	45	W
Operating Junction Temperature Range	T _J	-55 ~ +150	°C
Storage Temperature Range	T _{STG}	-55 ~ +150	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	R _{θJC}	-	2.6	2.8	°C/W
Thermal Resistance Junction-to-Air *1	R _{θJA}	-	65	87	°C/W

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -30V, V _{GS} = 0V	-	-	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *2	V _{GS} = -10V, I _D = -10A	-	13	20	mΩ
		V _{GS} = -4.5V, I _D = -6A	-	20	32	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.7	-2.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	9	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V	-	1406	-	pF
C _{OSS}	Output Capacitance	V _{DS} = -15V	-	157	-	
C _{RSS}	Reverse Transfer Capacitance	f = 1.0MHz	-	142	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time *4	V _{DD} = -15V	-	12	-	ns
t _r	Turn-on Rise Time *4	V _{GS} = -10V	-	18	-	
t _{d(OFF)}	Turn-Off Delay Time *4	I _D = -20A	-	90	-	
t _f	Turn-Off Fall Time *4	R _G = 3Ω	-	63	-	
Q _G	Total Gate-Charge (V _{GS} = -4.5V)	V _{DD} = -15V V _{GS} = -10V I _D = -10A	-	15	-	nC
	Total Gate-Charge (V _{GS} = -10V)		-	30.5	-	
Q _{GS}	Gate to Source Charge		-	4.6	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	5.7	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *2	I _{SD} = -10A, V _{GS} = 0V	-	-0.87	-1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} = -10A, V _{GS} = 0V	-	92	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt = 100A/μs	-	62	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The E_{AS} data shows Max. rating. The test condition is $V_{DD} = -20V, V_{GS} = -10V, L = 0.5mH$
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

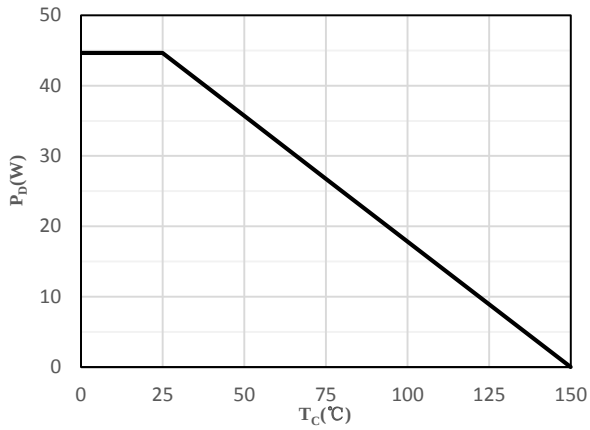


Fig 1 Power Dissipation

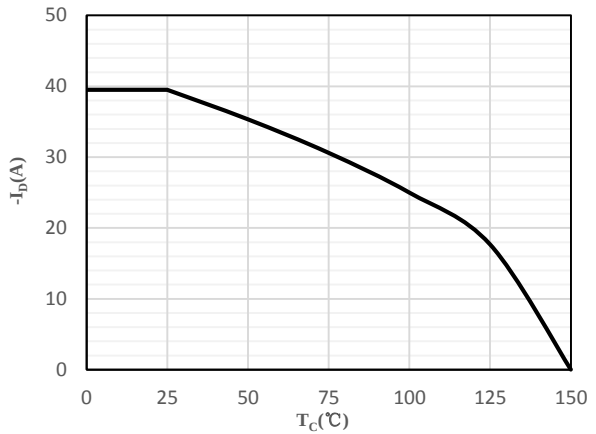


Fig 2 Drain Current

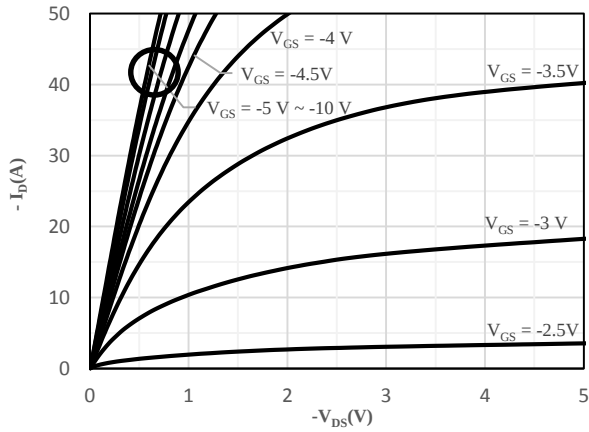


Fig 3 Typical Output Characteristics

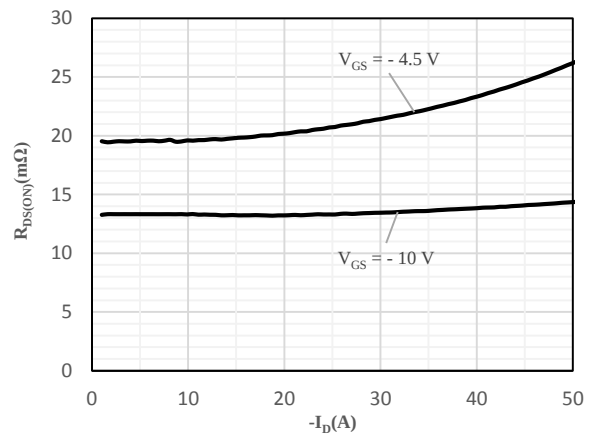


Fig 4 On-Resistance vs. Drain Current
and Gate Voltage

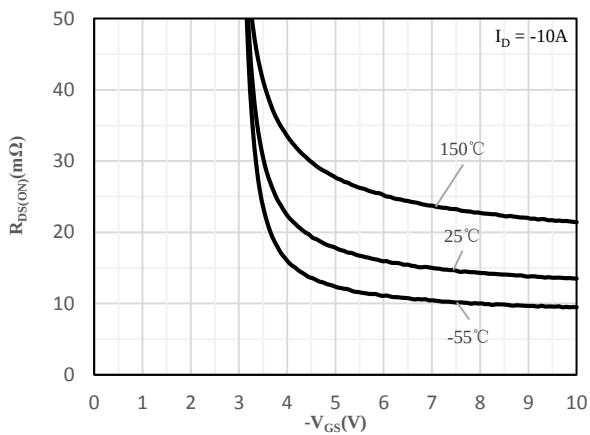


Fig 5 On-Resistance vs. Gate-Source Voltage

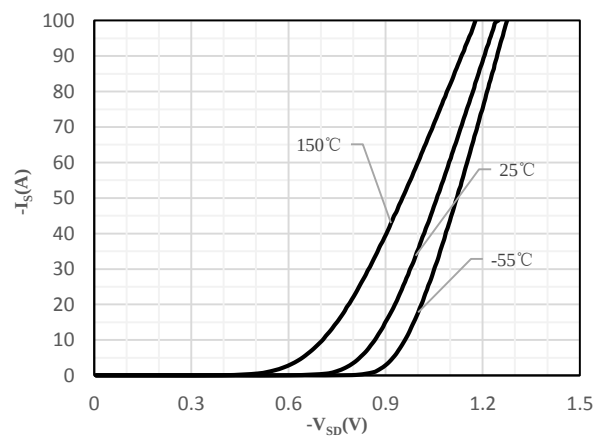


Fig 6 Body-Diode Characteristics

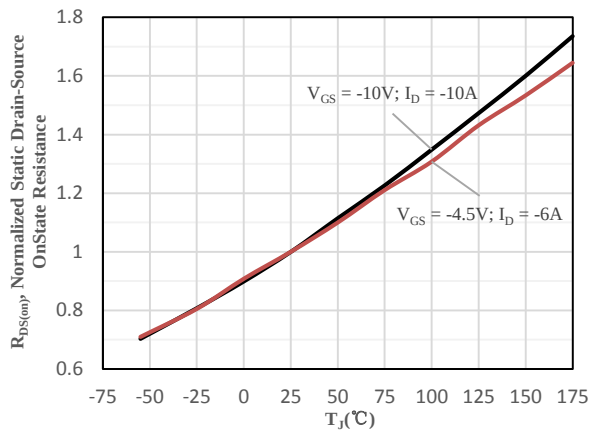


Fig 7 Normalized On-Resistance vs. Junction Temperature

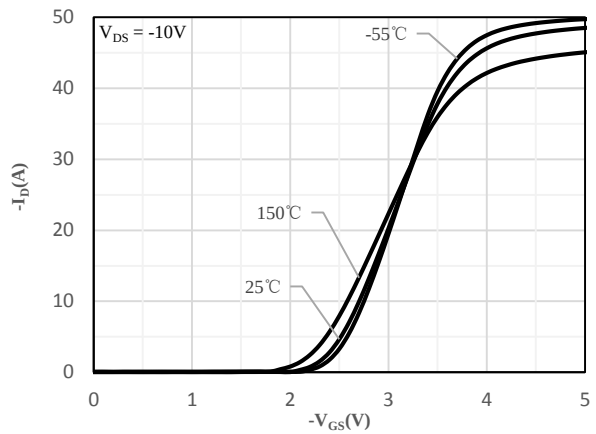


Fig 8 Transfer Characteristics

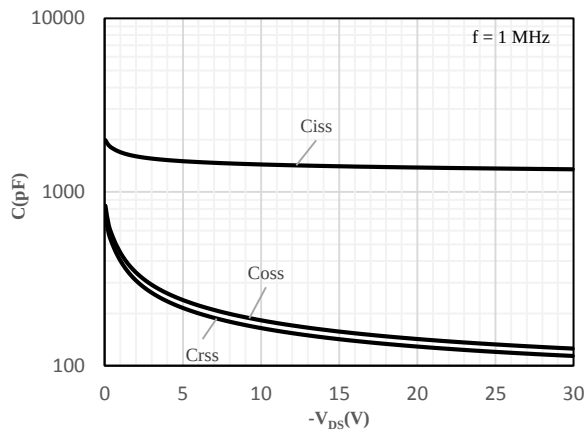


Fig 9 Capacitance Characteristics

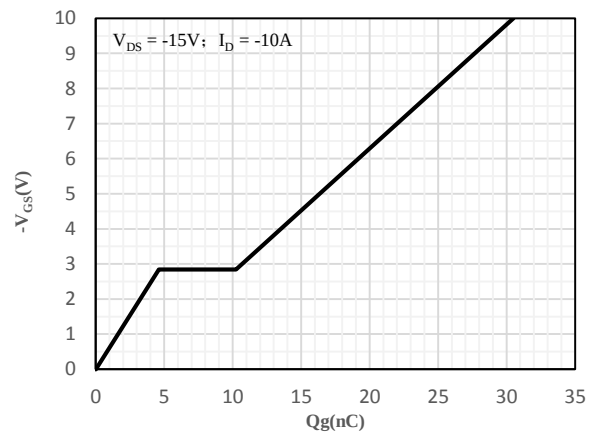


Fig 10 Gate-Charge Characteristics

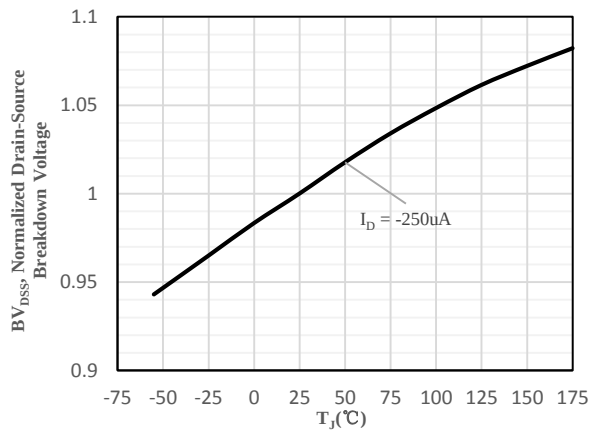


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

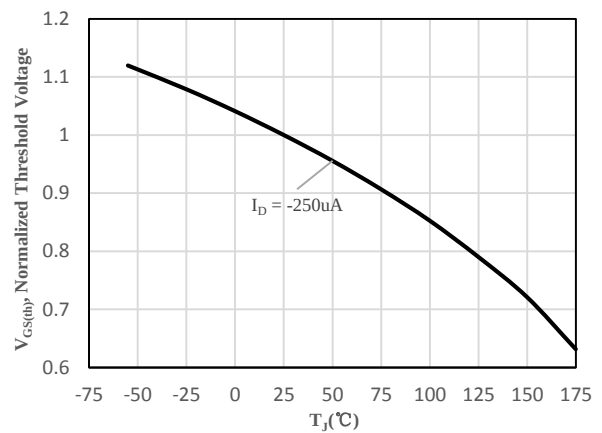


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

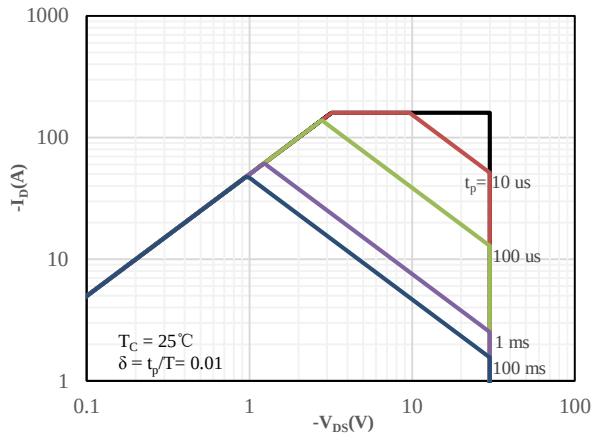


Fig 13 Safe Operating Area

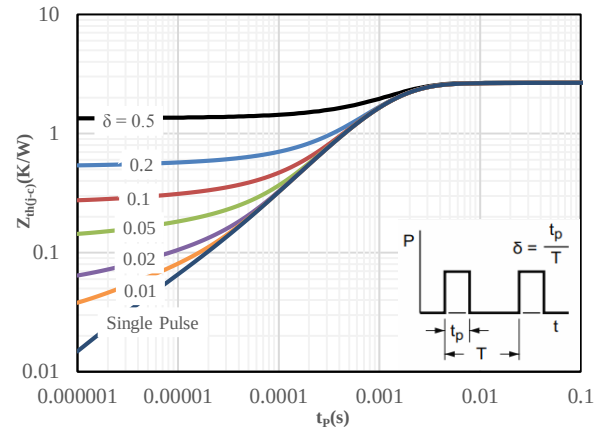
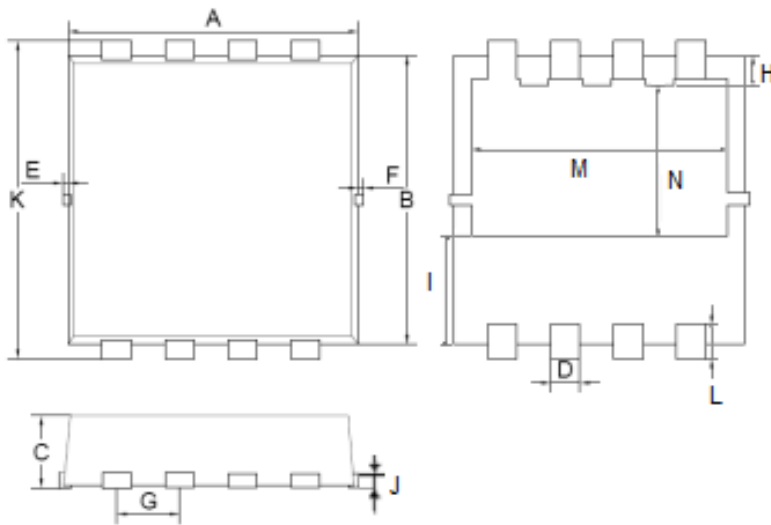


Fig 14 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)



PDFN3×3-8L		
Dimension	Min.	Max.
A	2.90	3.10
B	2.90	3.10
C	0.65	0.85
D	0.20	0.40
E	0.00	0.10
F	0.00	0.10
G	0.55	0.75
H	0.20	0.40
I	0.90	1.30
J	0.10	0.20
K	3.15	3.45
L	0.20	0.40
M	2.35	2.55
N	1.500	1.900

Mounting Pad Layout (Unit: mm)

PDFN3×3-8L

